



Docket No.: 067161-0117

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Pre Application of

Shinji TANAKA

Application No.: 10/682,153

Filed: October 10, 2003

: Customer Number: 20277

: Confirmation Number: 8196

: Group Art Unit: 2824

: Examiner: Hien N. Nguyen

For: SEMICONDUCTOR MEMORY DEVICE INPUTTING/OUTPUTTING DATA AND PARITY DATA IN BURST OPERATION

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

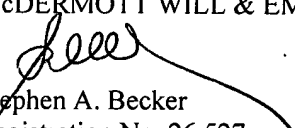
This Information Disclosure Statement is being filed under Rule 37 CFR 1.97(i), wherein applicants are submitting references before the grant of a patent to be placed in the file.

Accordingly, copies of the references as listed on the attached Form PTO-1449 are submitted herewith for placement in the file. No certification or fees are deemed necessary.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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